

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10759936			
<b>Filing Date:</b>	16-Jan-2004			
<b>Title of Invention:</b>	Method and apparatus for VLSI clock gated power estimation using LCB counts			
<b>First Named Inventor/Applicant Name:</b>	Rajat Chaudhry			
<b>Filer:</b>	Gregory W. Carr/Bradley Ellis			
<b>Attorney Docket Number:</b>	AUS920030974US1			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Filing a brief in support of an appeal	1402	1	510	510
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				510